

FIG. 1 PRIOR ART

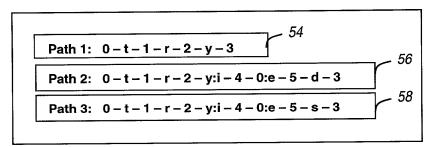


FIG. 2 PRIOR ART

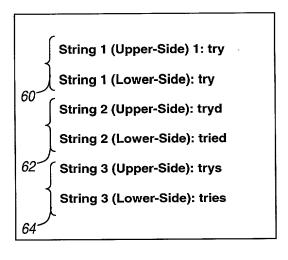


FIG. 3 PRIOR ART

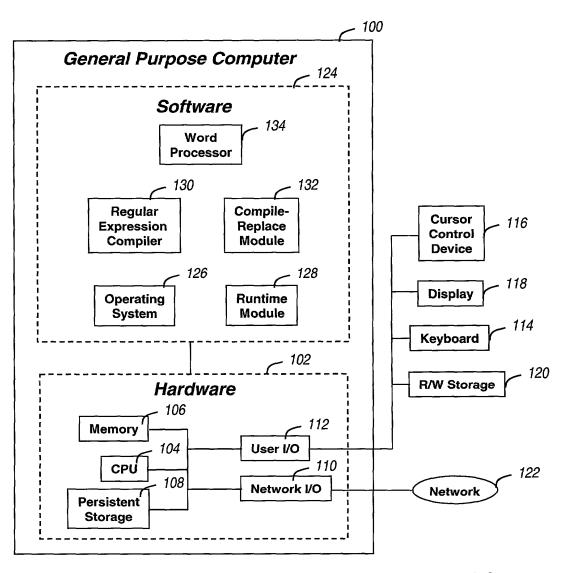


FIG. 4

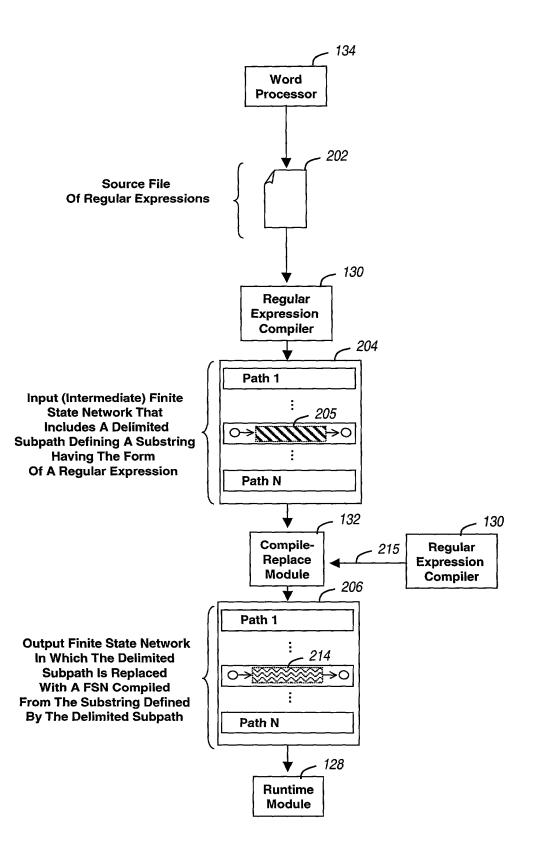


FIG. 5

FIG. 6

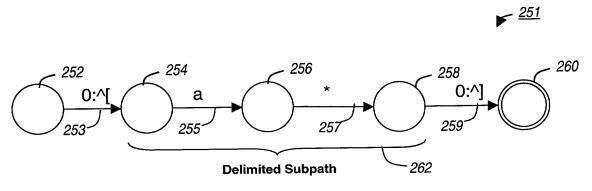
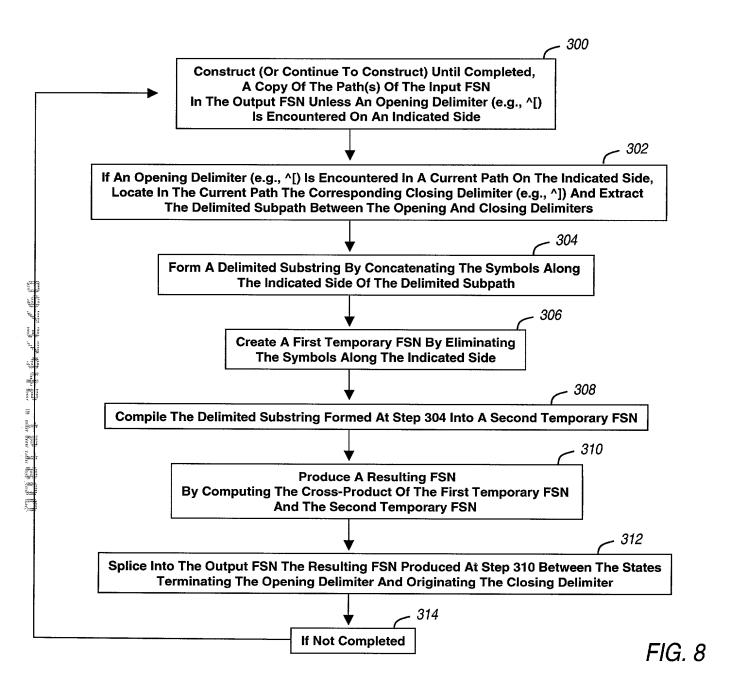
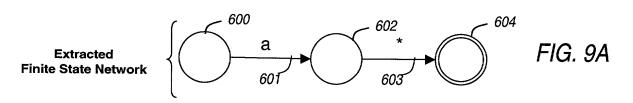
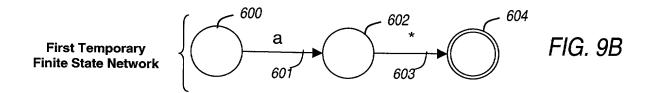
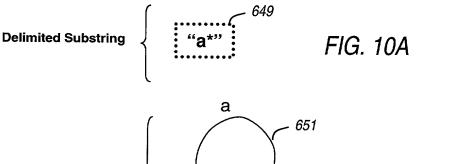


FIG. 7



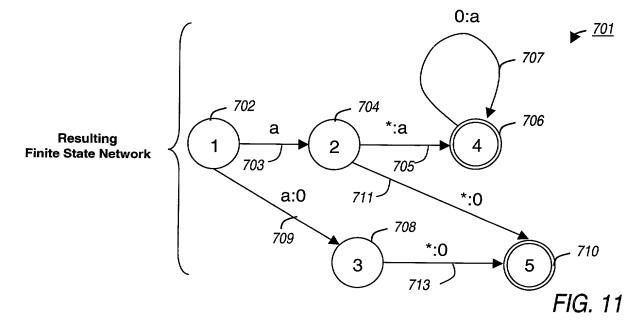


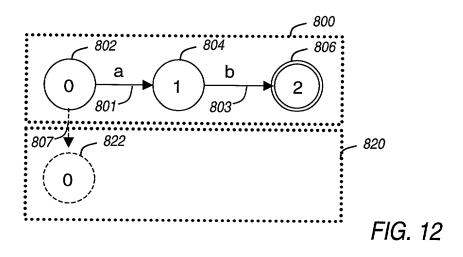


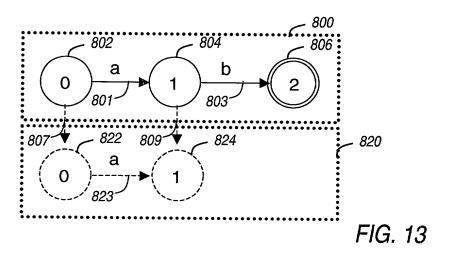


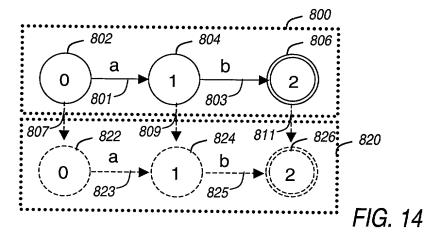
Second Temporary
Finite State Network

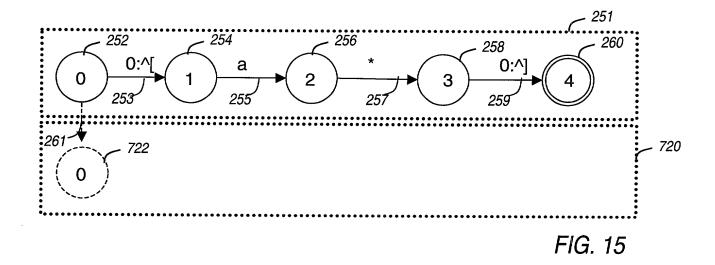
FIG. 10B











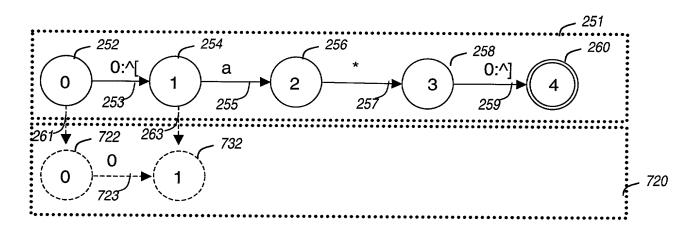
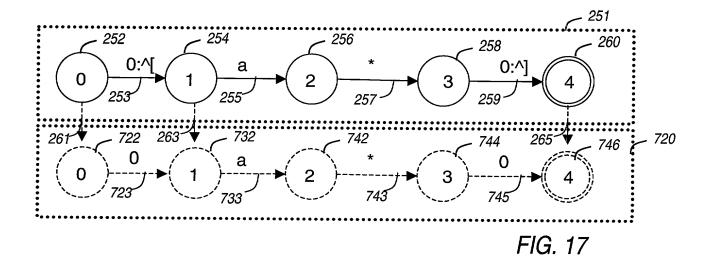


FIG. 16



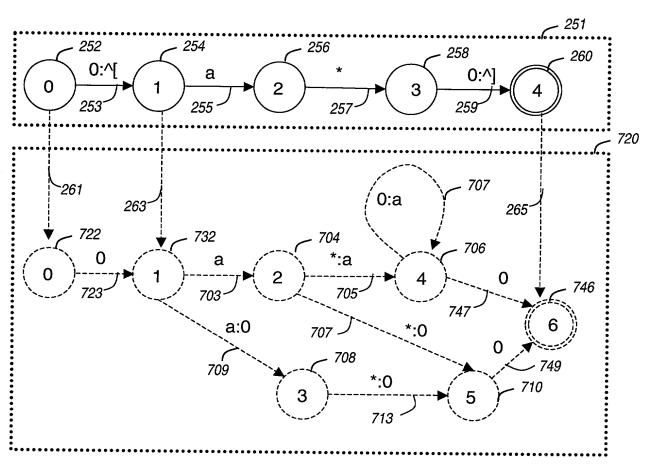


FIG. 18